

Figure 1
programmable
logic device 10

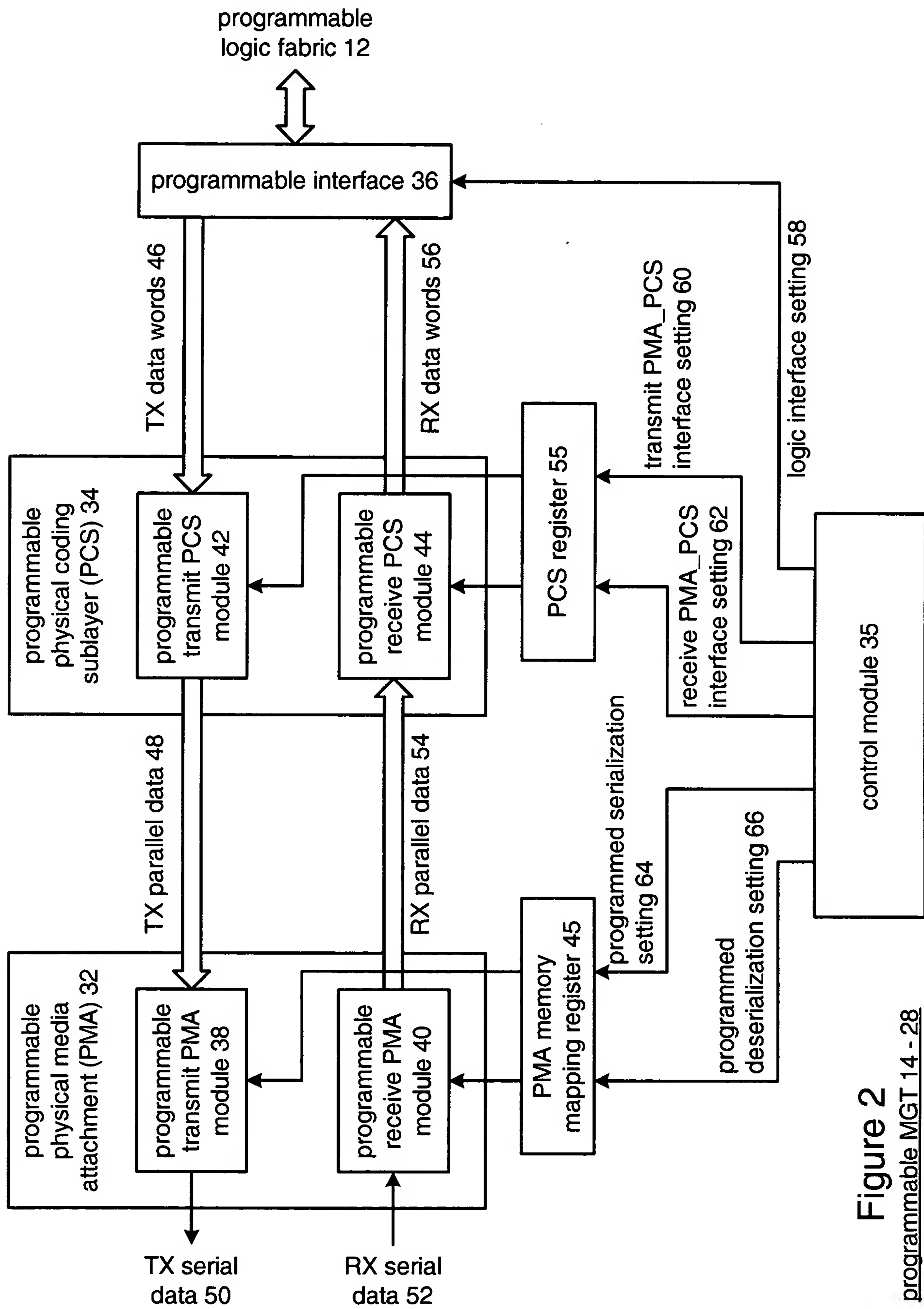


Figure 2
programmable MGT 14 - 28

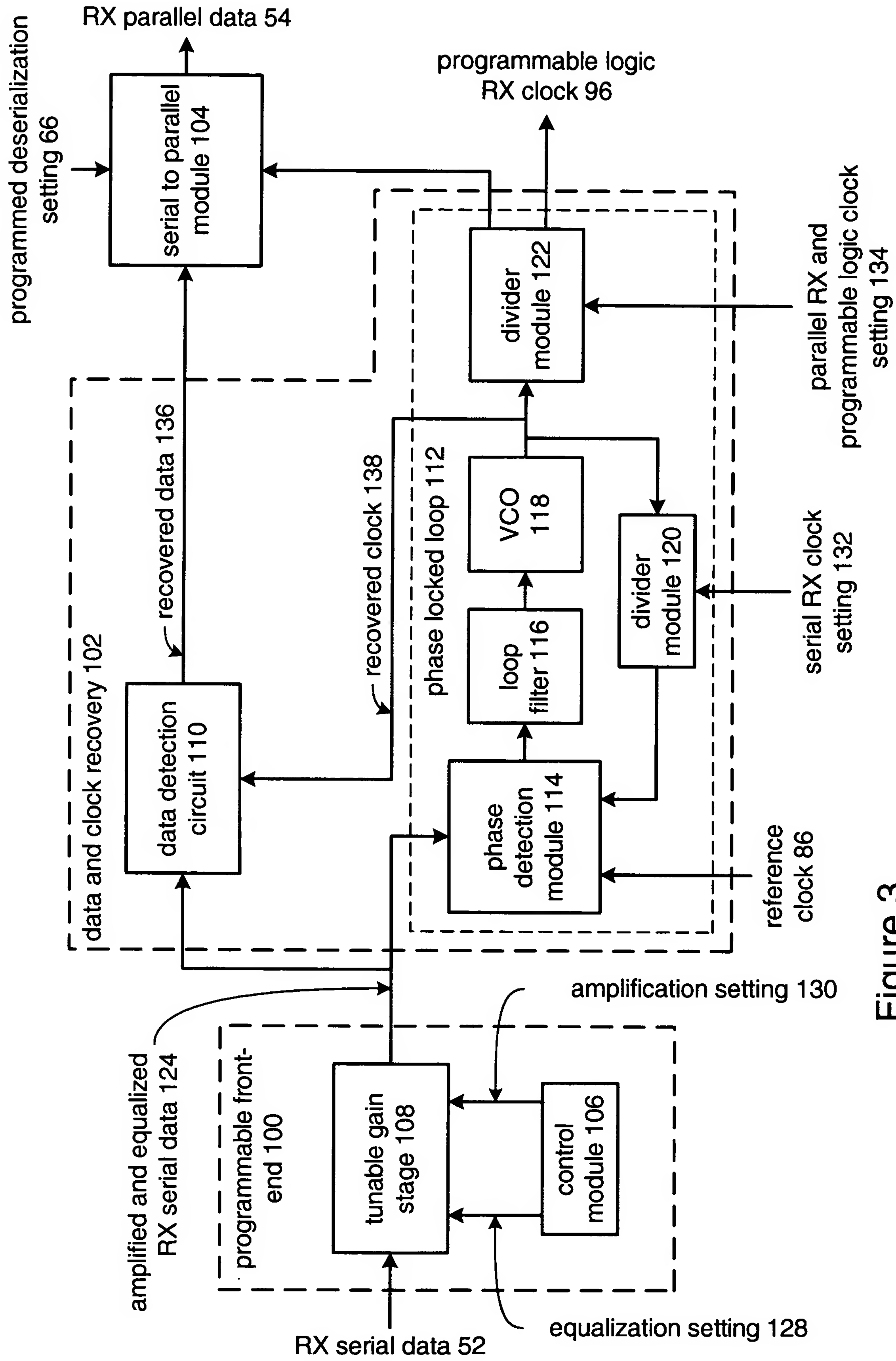


Figure 3
programmable receive
PMA module 40

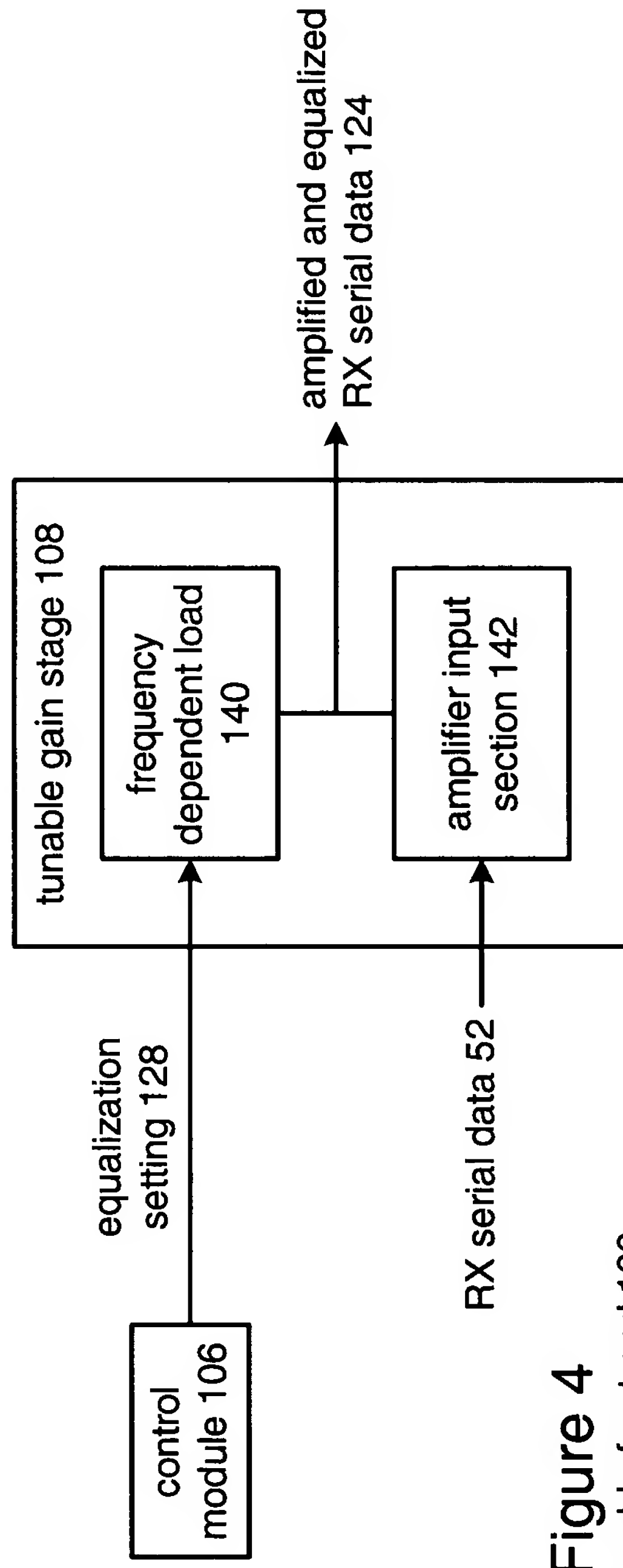


Figure 4
programmable front-end 100

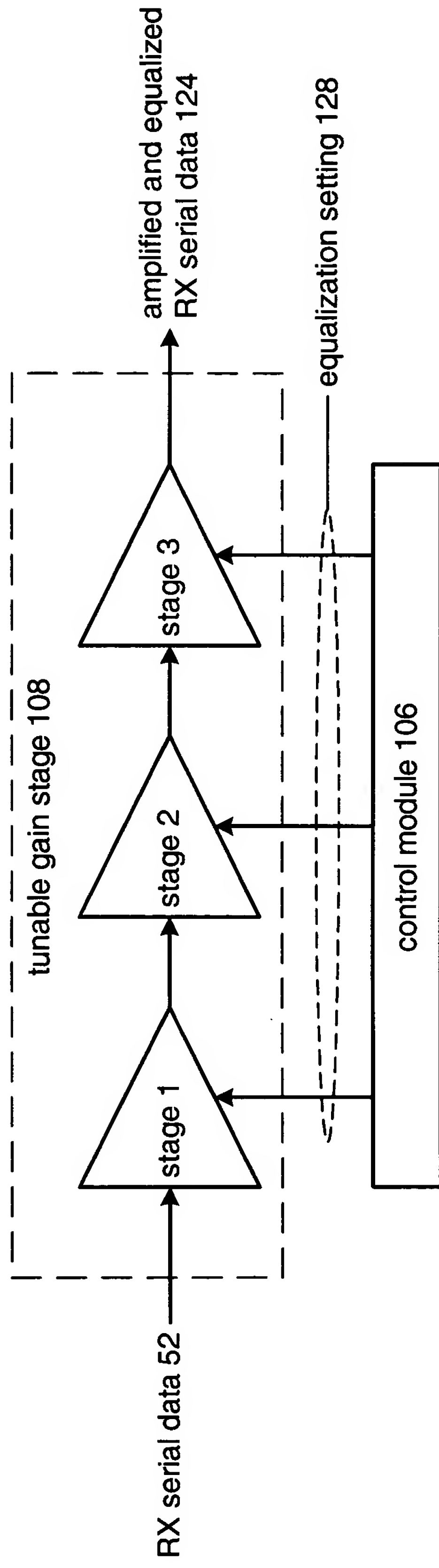


Figure 5
programmable front-end 100

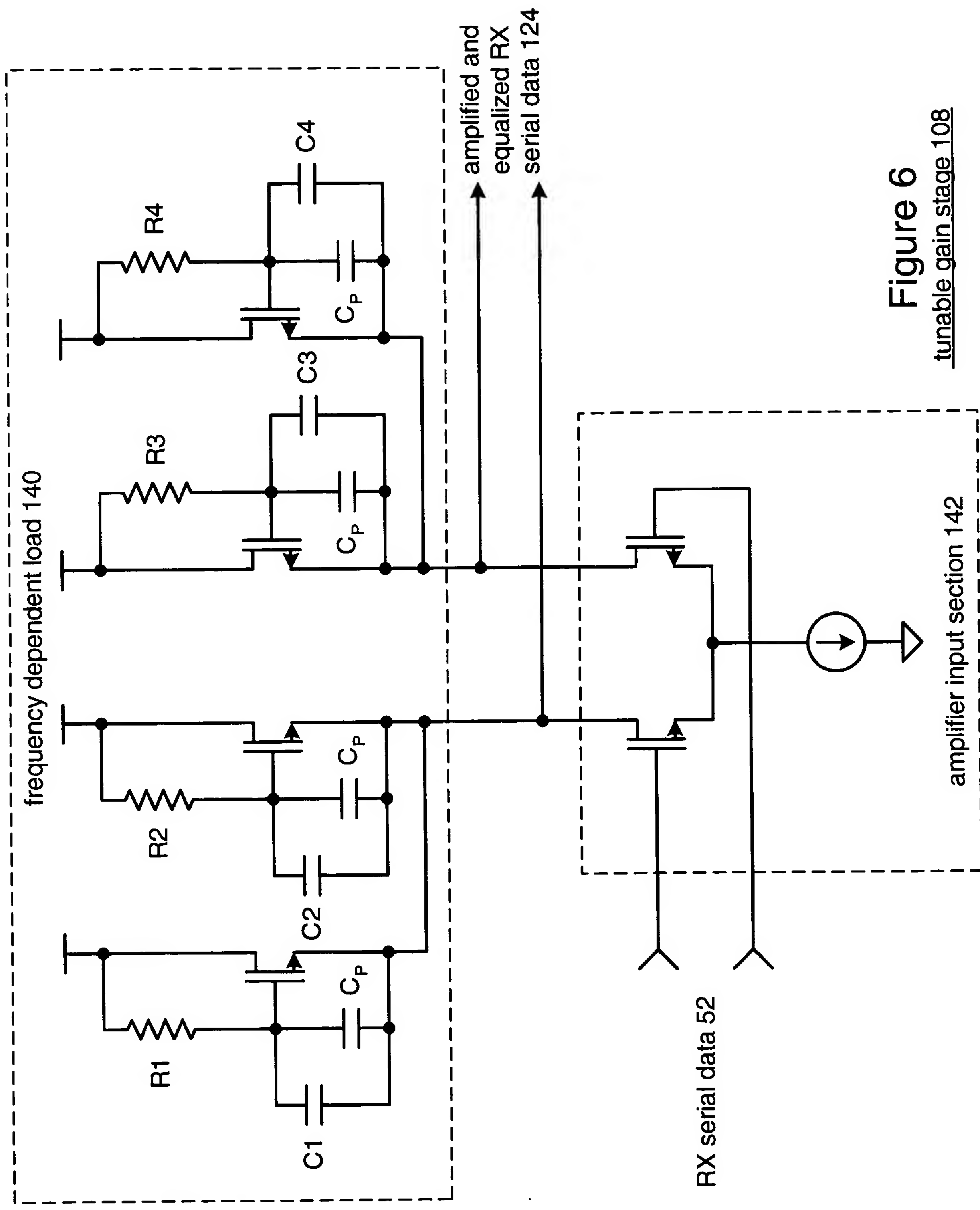


Figure 6
tunable gain stage 108

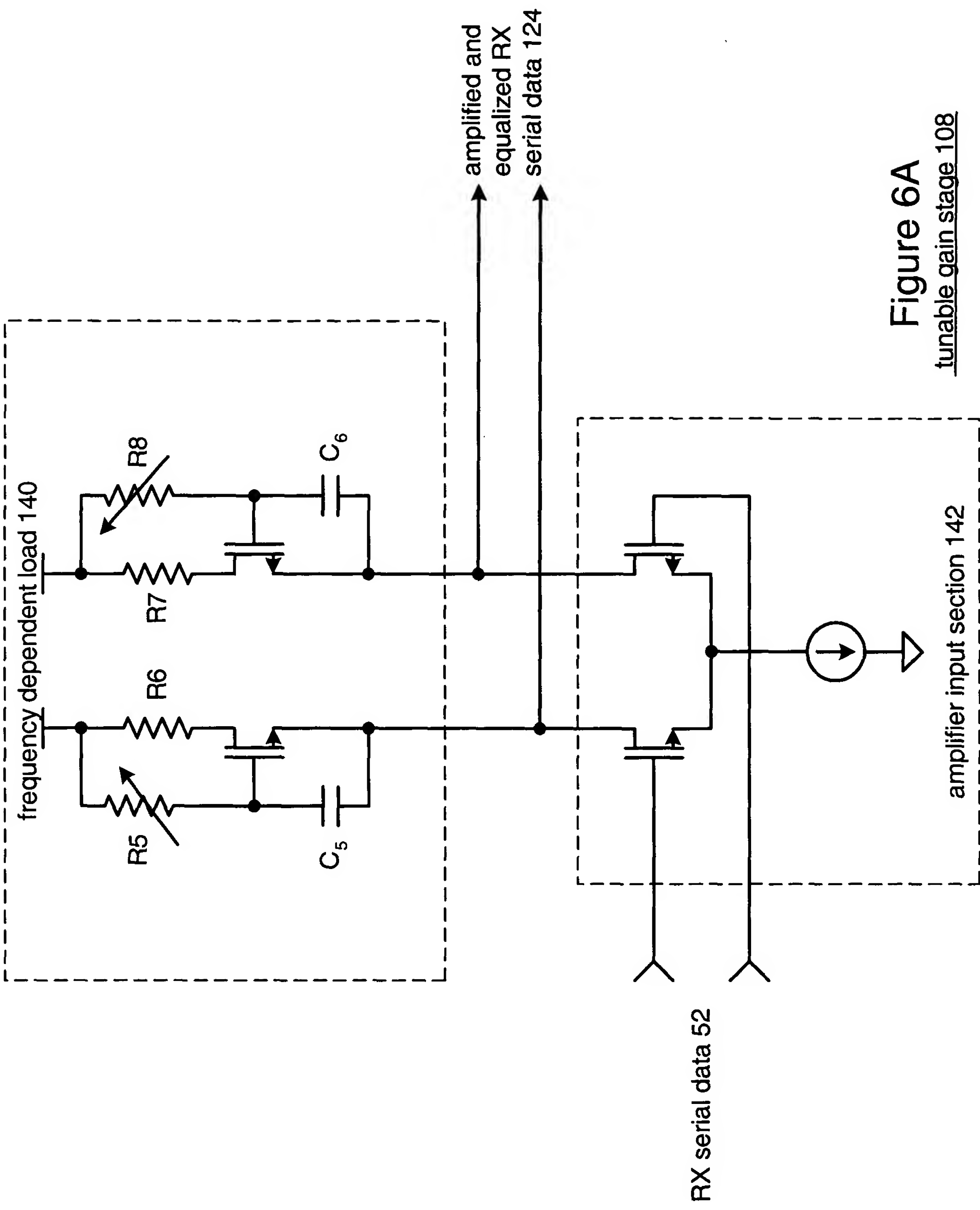


Figure 6A
tunable gain stage 108

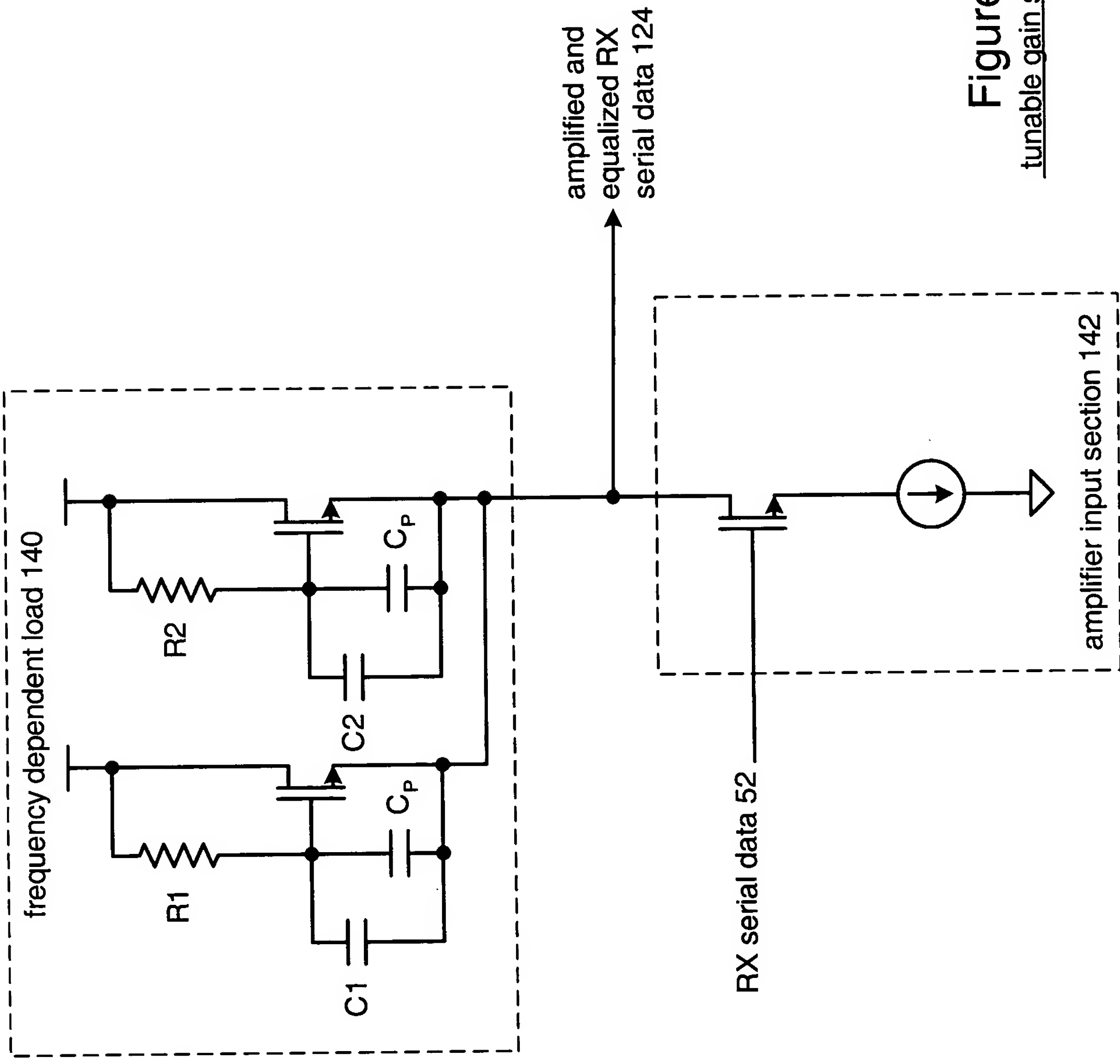


Figure 7
tunable gain stage 108

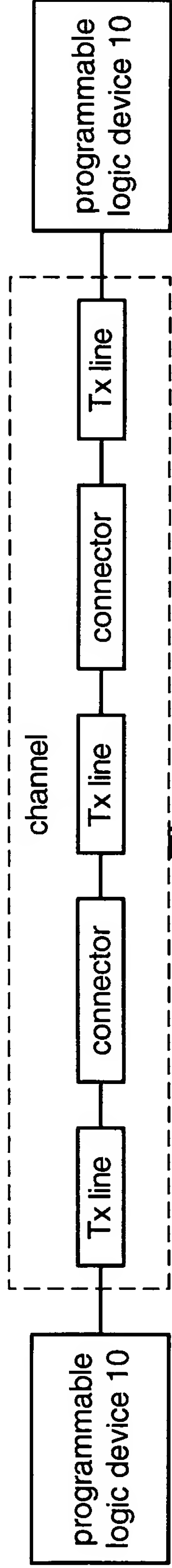


Figure 8A

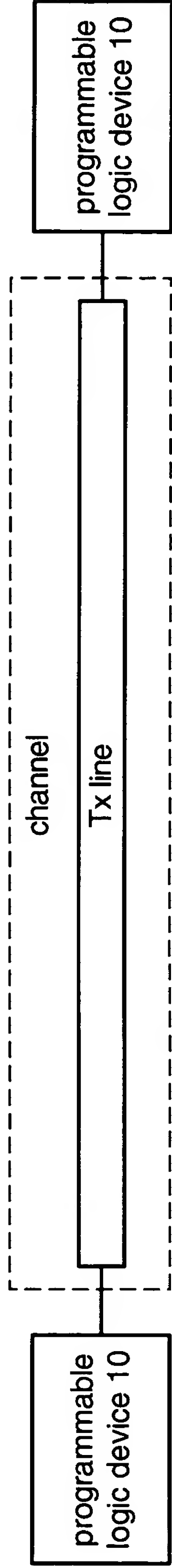


Figure 8B

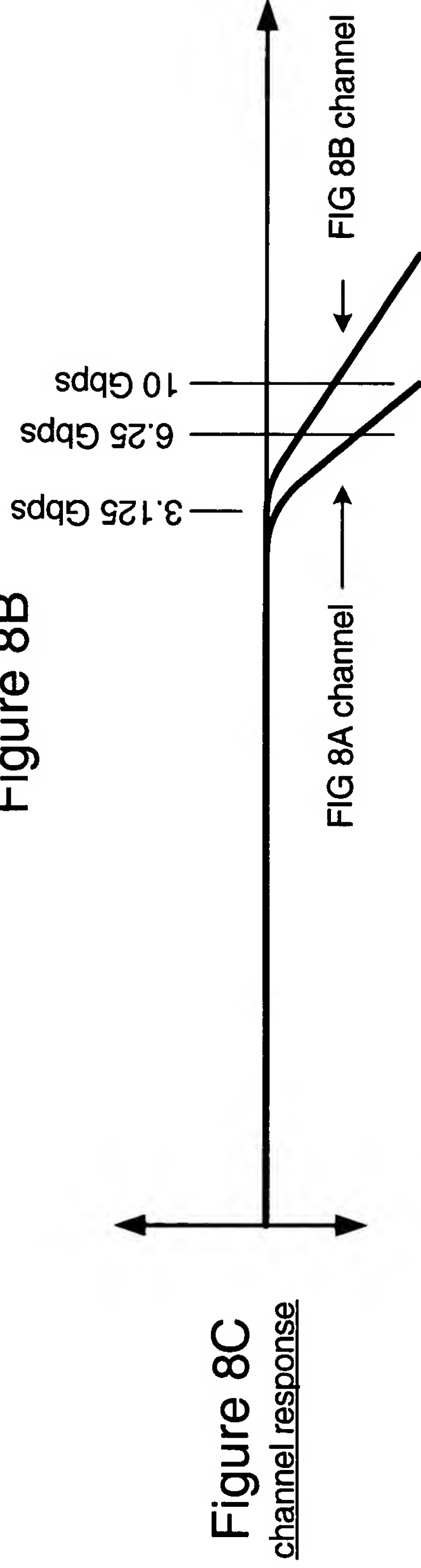


Figure 8C
channel response

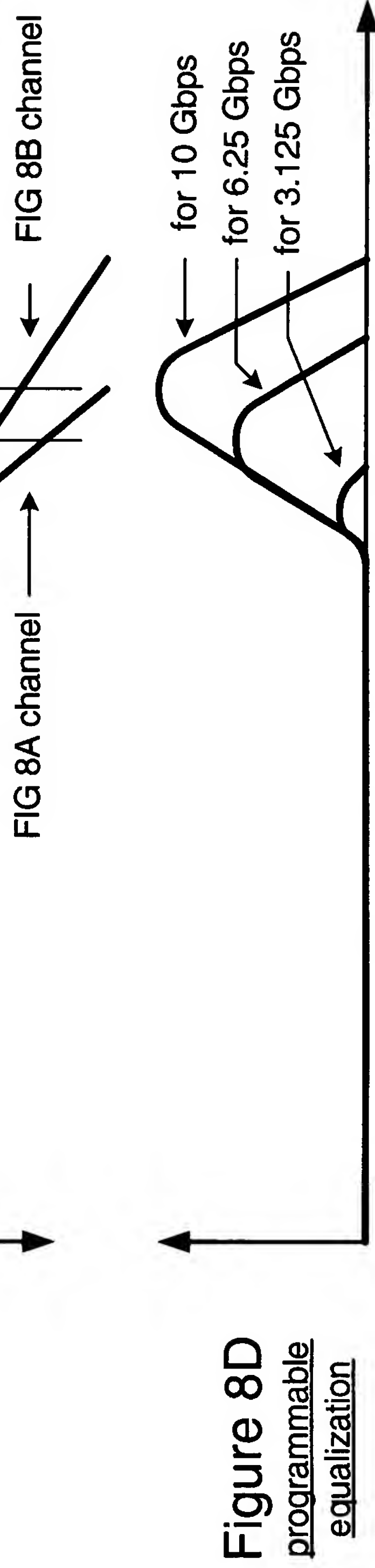


Figure 8D
programmable
equalization